

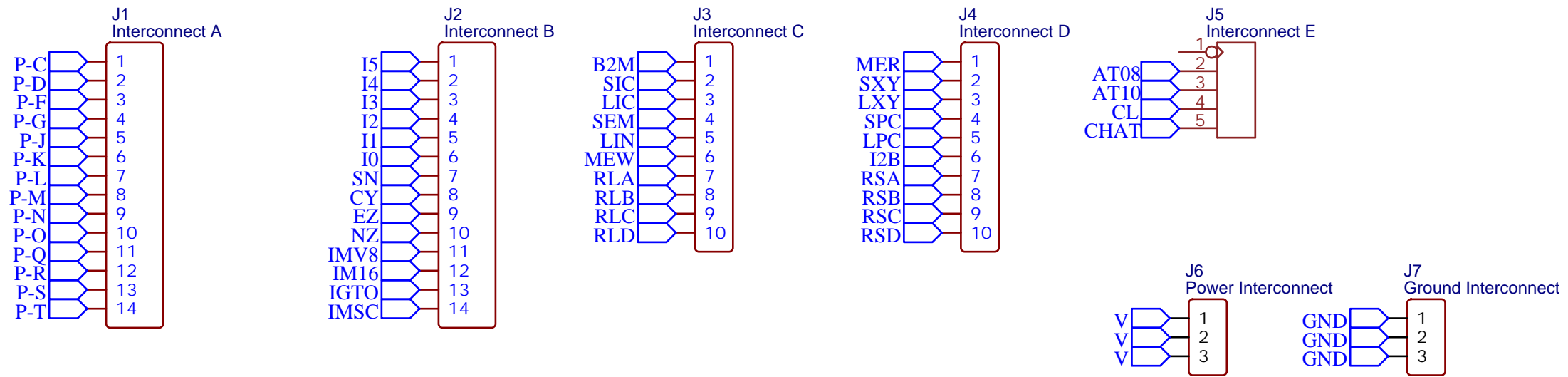
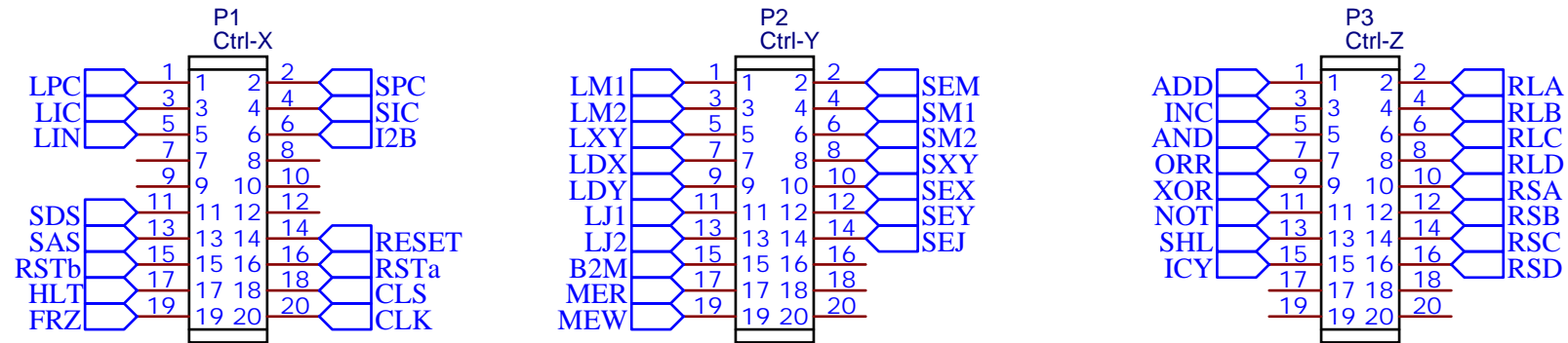
GOTO: 11dscznx
 d = dest (M or J)
 s = sign (ignore or load PC)
 c = carry (ignore or load PC)
 z = zero (ignore or load PC)
 n = non-zero (ignore or load PC)
 x = PC to XY (no action or copy)

Note: SN/CY/EZ/NZ have inbound diode on upper board

P-* signals connect from Pulse Bus via upper card (Sheet 3)
 I0-17 signals connect from C/I Bus via upper card (Sheet 3)
 CHAT signal connects from Fetch/Increment group via upper card (Sheet 3)
 AT08/10/12/14 and IGTO signals connect to OP Bus via upper card (Sheet 3)
 Other signals connect to CtrIX/Y/Z (Sheet 3)

TITLE: Relay Computer : Controller Lower Card		REV: 1.0
EasyEDA	Company: Paul Law	Sheet: 1/4
	Date: 2019-11-02	Drawn By: paul_6392

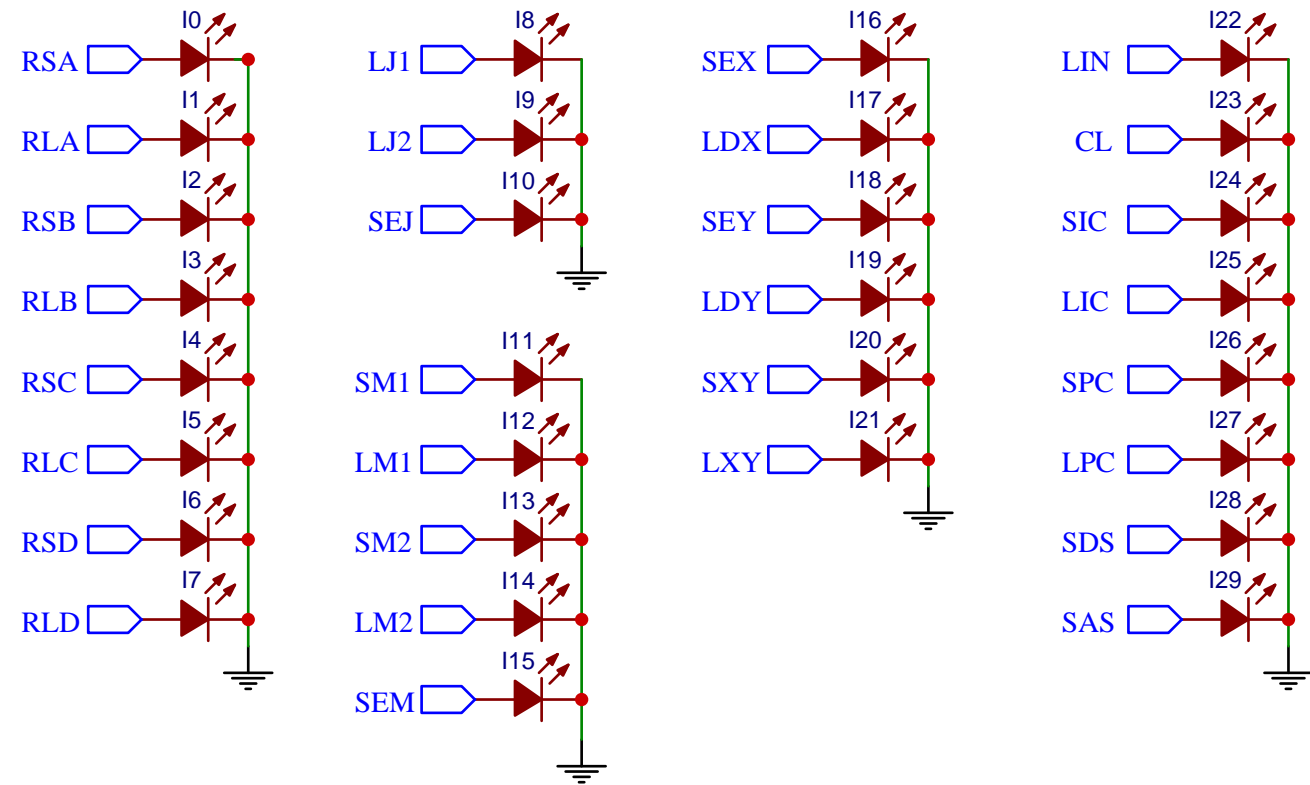
Sheet 3: Backplane & Inter-card connections



Interconnects pass signals to/from upper card
P1-P3 connect to W2 backplane connectors

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Sheet 4: Card Status LEDs



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