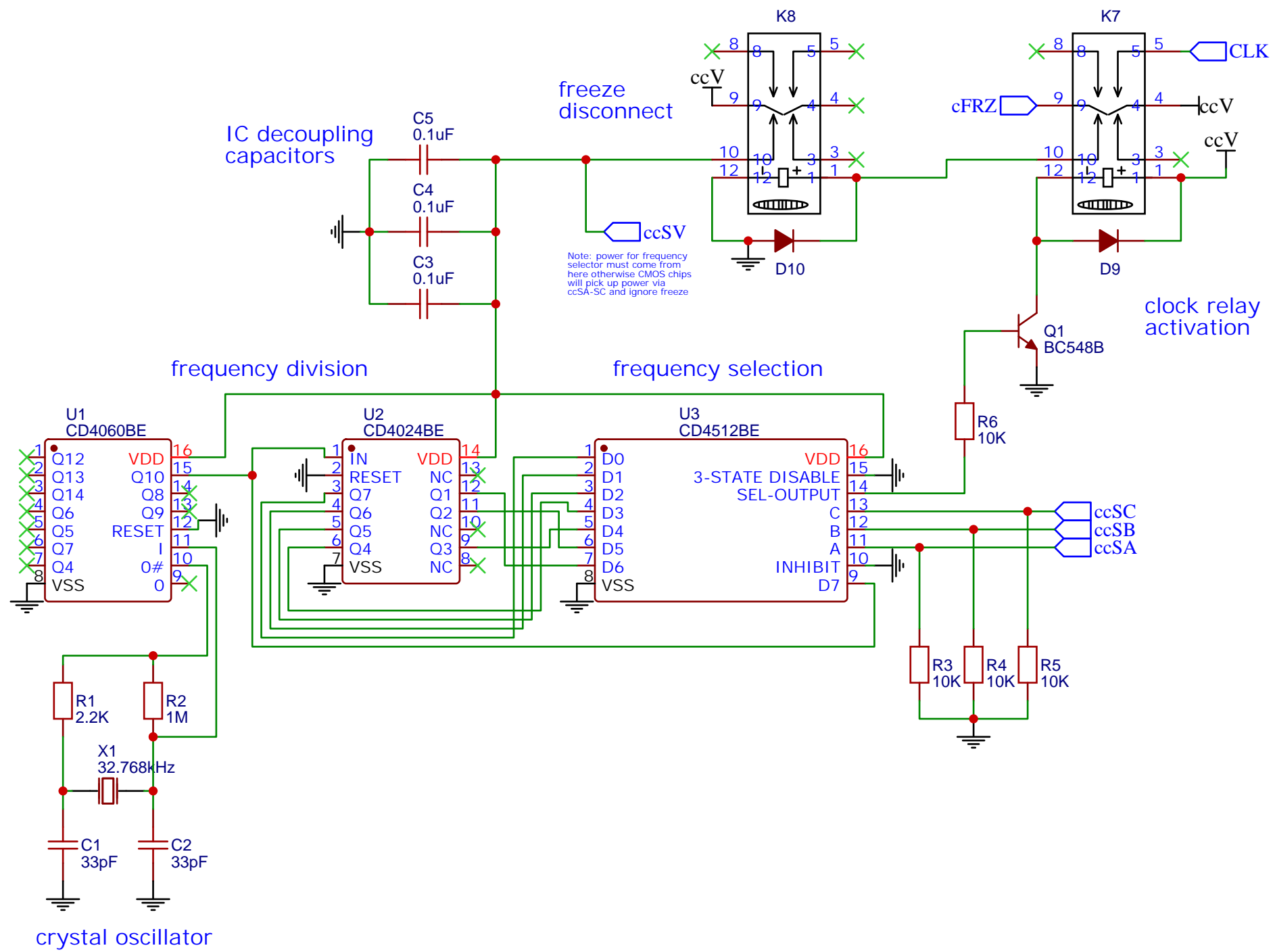


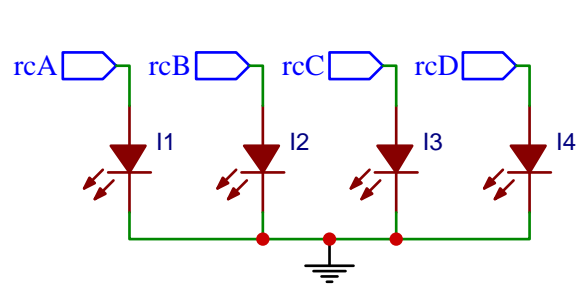
rcA-D, CLK and cFRZ connect to UI (sheet 3)  
rcV is fed from UI (sheet 3)

TITLE: Relay Computer : Clock Card		REV: 1.1
	Company: Paul Law	Sheet: 1/4
	Date: 2022-10-15	Drawn By: Paul Law

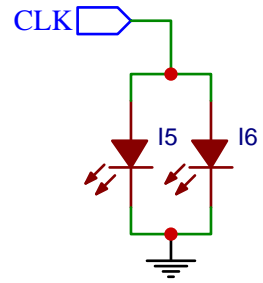


ccSA-SC, ccSV, CLK and cFRZ connect to UI (sheet 3)  
 ccV is fed from UI (sheet 3)

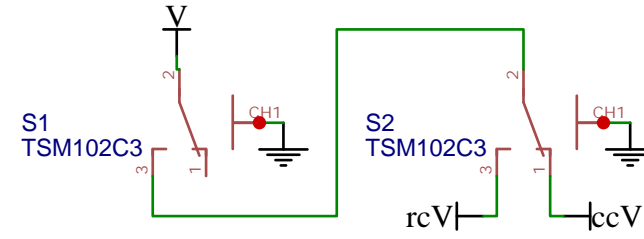
TITLE: Relay Computer : Clock Card		REV: 1.1
	Company: Paul Law	Sheet: 2/4
	Date: 2022-10-15	Drawn By: Paul Law



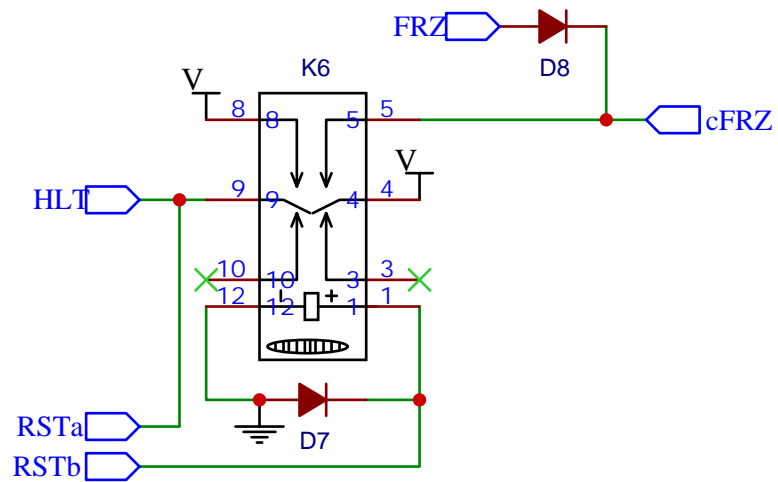
relay clock stages



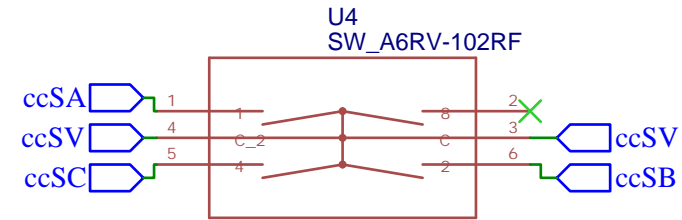
clock output



clock power & relay/crystal switch



halt latch



frequency selection (crystal clock)

Important Note: All LEDs shown are 12V type (with integral resistor)

HLT, FRZ and CLK connect to backplane (sheet 4)

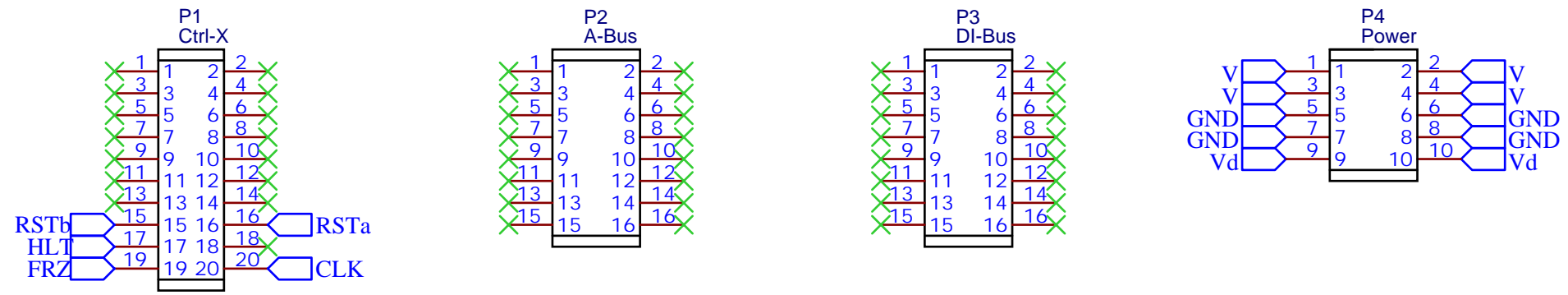
rcA-D, CLK and cFRZ connect to Relay Clock (sheet 1)

ccSA-SC, ccSV, CLK and cFRZ connect to Crystal Clock (sheet 2)

rcV and ccV feeds to Relay and Crystal clock respectively (sheet 1 and 2)

TITLE: Relay Computer : Clock Card		REV: 1.1
	Company: Paul Law	Sheet: 3/4
	Date: 2022-10-15	Drawn By: Paul Law

Sheet 4: Backplane connections



HLT, FRZ and CLK connect to UI (sheet 3)

TITLE: Relay Computer : Clock Card		REV: 1.1
	Company: Paul Law	Sheet: 4/4
	Date: 2022-10-15	Drawn By: Paul Law